Faculty of Electrical and Computer Engineering
Examination office

02.02.2026

Examination plan for **WiSe 25/26**

Master´s program **Nanoelectronic Systems**
3. Semester

only modules for re-examination!

Examination periode from **09.02. bis 07.03.2026**

(subject to further changes)

Examination subject	Date	Professor	Place*	Time Duration
Compulsory modules				
Academic and Scientific Work (NES-12 ASW-14.1) M1200-50010 Selection 1 out of 3: **Paper Reading Group**				
Mündliche Prüfungsleistung (oral exam)	09.02.2026 - 11.02.2026	Prof. Mikolajick	MIE 116	07:30 Uhr 45 min
Präsentation (presentation)	on appoint.		-	-
Photonic Devices Group				
Beleg (assigned paper)	on appoint.	Prof. Jamshidi	-	-
Präsentation (presentation)	on appoint.		-	-
Nanoelectronic Systems Design Student Conference				
Beleg (assigned paper)	on appoint.	Prof. Fettweis	-	-
Präsentation (presentation)	on appoint.		-	-
Project Work (NES-12 PW-14.1) M1200-50060 No registration at SELMA required	on appoint.	Prof. Mikolajick	-	-
Optional modules				

Innovative Concepts for Active Nanoelectronic Devices (NES-ET-22-E-ICAND) M1212-50063				
Klausurarbeit/Mündliche Prüfungsleistung Innovative Semiconductor Devices (oral or written exam)	27.02.2026	Prof. Mikolajick	BAR/I88/U	09:20 Uhr 90 min
Klausurarbeit/Mündliche Prüfungsleistung Materials for Nanoelectronics (oral or written exam)	19.02.2026	Prof. Richter	M13/DÜLF/U	13:00 Uhr 90 min
Protokollsammlung (protocols)	on appoint.	Prof. Richter	-	-
Communication Networks 3 (NES-12 10 20) M1210-1I280				
Klausurarbeit/Mündliche Prüfungsleistung (oral or written exam)	02.03.2026	Jun.-Prof. Bassoli	BAR/I88/U	09:20 Uhr 120 min
Projektarbeit (project work)	on appoint.	Jun.-Prof. Bassoli	-	-
Integrated Photonic Devices for Communications and Signal Processing (NES-12 10 06-14.1) M1210-50170				
Beleg (assigned paper)	on appoint.	Prof. Jamshidi	-	-
Mündliche Prüfungsleistung (oral exam)	on appoint.	Prof. Jamshidi	-	- 30 min
Memory Technology (NES-12 12 03-14.1) M1212-1M140	on appoint.	Prof. Mikolajick	-	- 15 min

Materials for the 3D System Integration (NES-12 06 01-14.1) M1206-50200 *Klausurarbeit Mikro-/Nanowerkstoffsysteme und Zuverlässigkeitsaspekte (written exam)* *Laborpraktikum Mikro-/Nanowerkstoffsysteme und Zuverlässigkeitsaspekte (lab course)*	on appoint. on appoint.	Prof. Panchenko Prof. Panchenko	- -	- 90 min -
Electromechanical Networks (NES-12 12 04-14.1) M1212-50130	09.02.2026	Prof. Marschner	ZEU/260/H	07:30 Uhr 120 min
Integrated Circuits for Broadband Optical Communications (NES-12 08 04-14.1) M1208-1I230	06.03.2026	Prof. Ellinger	BAR/SCHÖ/E	13:00 Uhr 120 min
Molecular Electronics (NES-13 14 02-14.1) M1200-50210	on appoint.	Prof. Erbe	-	- 20 min
Optoelectronics (NES-12 12 05-14.1) M1212-50230 *Klausurarbeit/Mündliche Prüfungsleistung Optoelectronic Devices and Systems (oral or written exam)* *Mündliche Prüfungsleistung Nanooptics (oral exam)*	on appoint. on appoint.	Prof. Lakner Prof. Eng	- -	- 20 min - 20 min
Quantum Mechanics for Nanoelectronics (NES-02 04 01) M1200-50240	on appoint.	Prof. Helm	-	- 30 min
Hardware Modelling and Simulation (NES-11 20 20) M1200-50380	16.02.2026	Prof. Göhringer	HSZ/304/Z	07:30 Uhr 60 min

Plasma Technology (NES-ET-E-PlaTe) M1211-50480	12.02.2026	Prof. von Hauff	HÜL/S186/H	07:30 Uhr 90 min
Foundations of Certified Programming Language and Compiler Design (NES-INF-E-FCPL) M1200-50460	on appoint.	Dr. Ertel	-	- 30 min
Computational Laser Systems (NES-ET-E-ComLS-23) M1208-50490	on appoint.	Prof. Czarske	-	- 30 min

Re-examination

Radio Frequency Integrated Circuits (NES-12 08 02-14.1) M1208-1I100	11.02.2026	Prof. Ellinger Dr. Schumann	HSZ/301/U	09:20 Uhr 120 min
Hardware/Software Codesign (NES-12 10 03-14.3) M1210-50030	24.02.2026	Prof. Fettweis Dr. Matus	GÖR/127/U	09:20 Uhr 120 min
Semiconductor Technology (NES-12 12 02-19.1) M1212-50080	04.03.2026	Prof. Mannsfeld Dr. Hambsch	HÜL/S186/H	09:20 Uhr 120 min
Communications (NES-12 10 02-14.1) M1210-50100	13.02.2026	Prof. Fettweis	GÖR/127/U	09:20 Uhr 120 min
Neuromorphic VLSI Systems (NES-12 08 06) M1208-1I360 *Beleg (assigned paper)*	on appoint.	Prof. Mayr	-	-
VLSI Processor Design (NES-12 08 7) M1208-50320 *Projektarbeit (project work)* *Referat (presentation)*	 on appoint. on appoint.	 Prof. Mayr Prof. Mayr	 - -	 - -

*** Floor plan: see notice board!**

signed Prof. Dr.-Ing. T. Mikolajick
Chairman of the Examination Board